

Fig. 1A

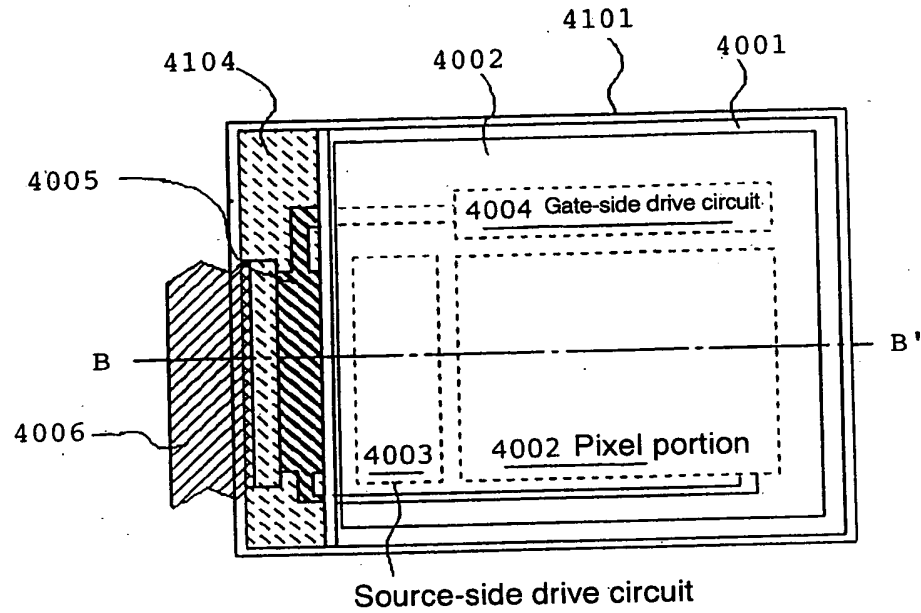


Fig. 1B

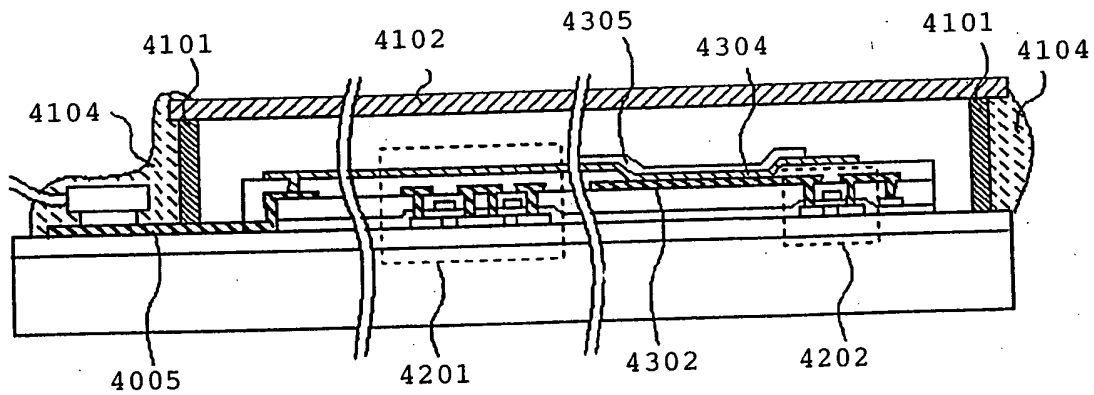


Fig. 2A

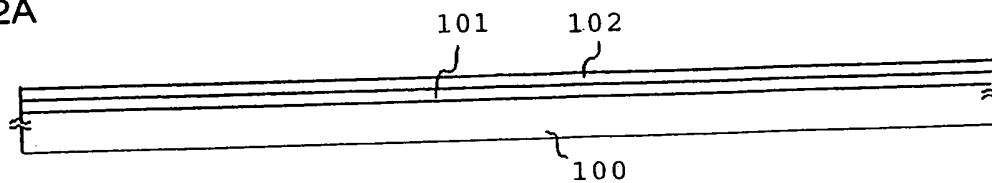


Fig. 2B

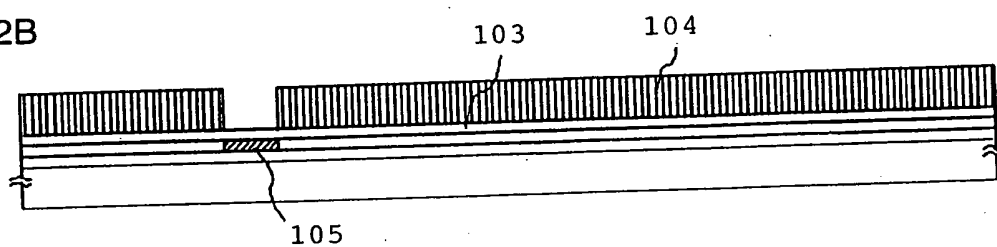


Fig. 2C

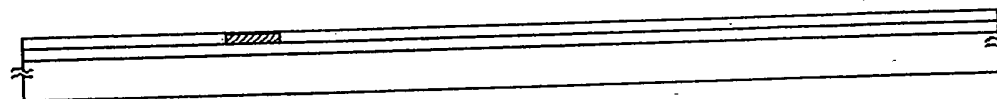


Fig. 2D

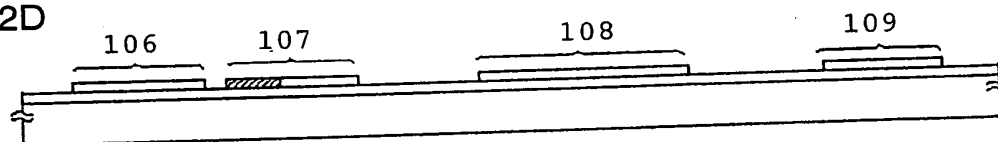


Fig. 2E

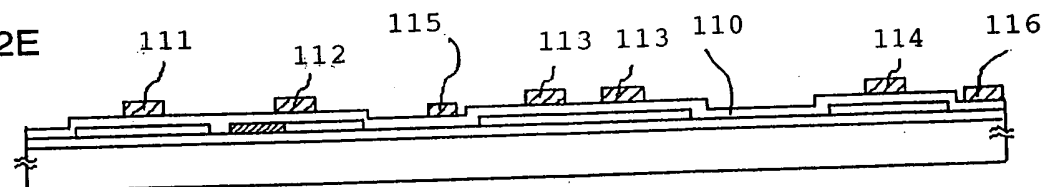


Fig. 3A

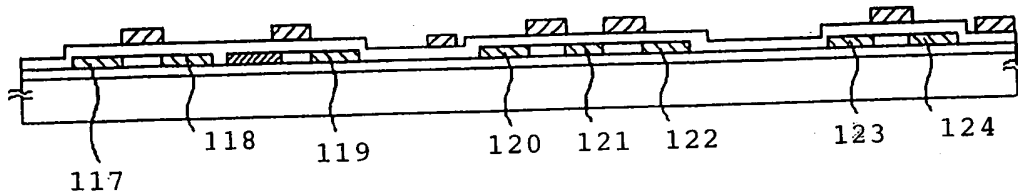


Fig. 3B

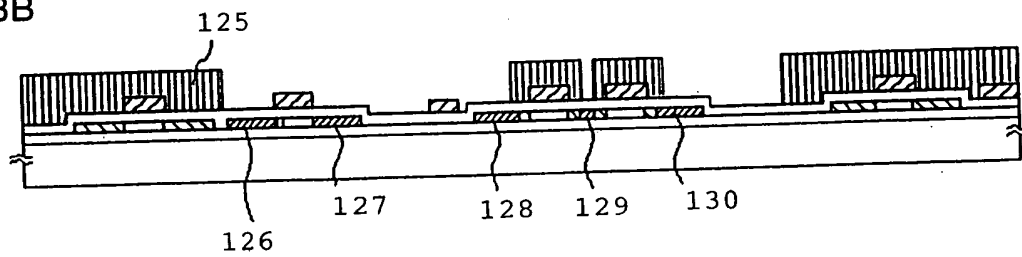


Fig. 3C

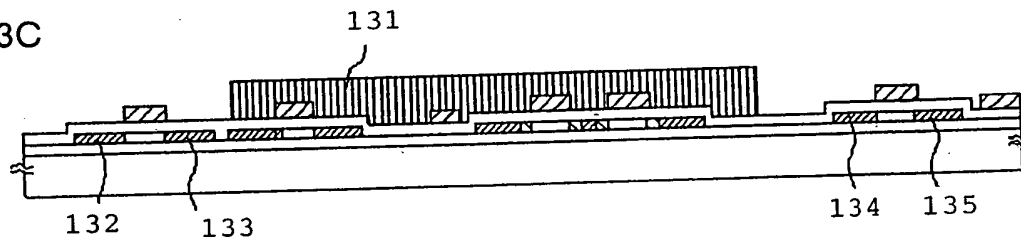


Fig. 3D

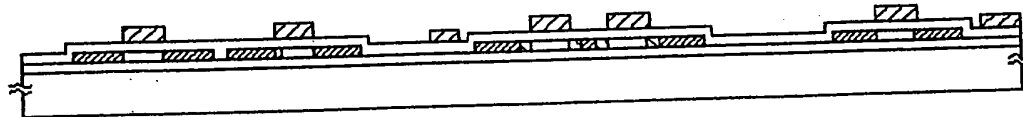


Fig. 4A

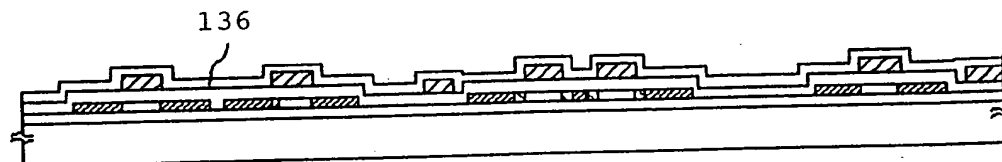


Fig. 4B

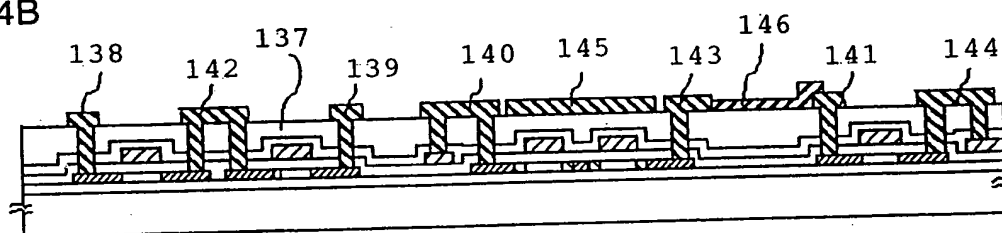


Fig. 4C

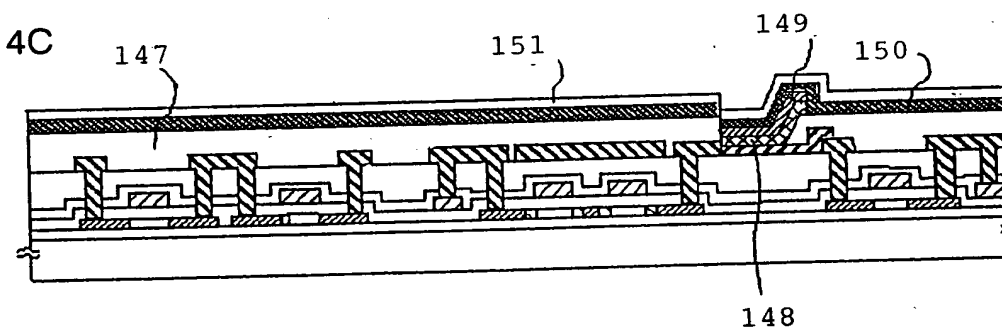


Fig. 5

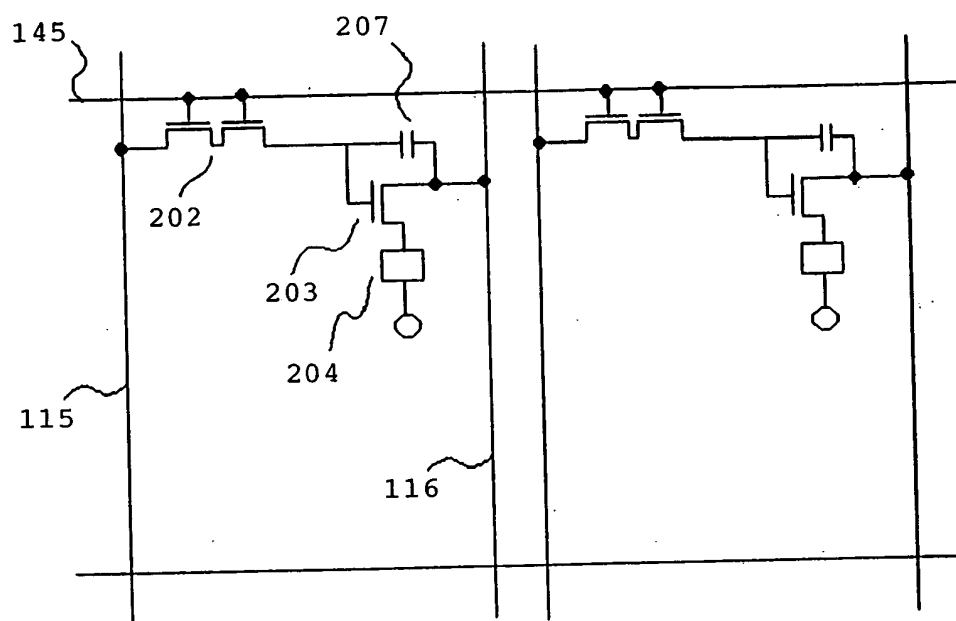


Fig. 6

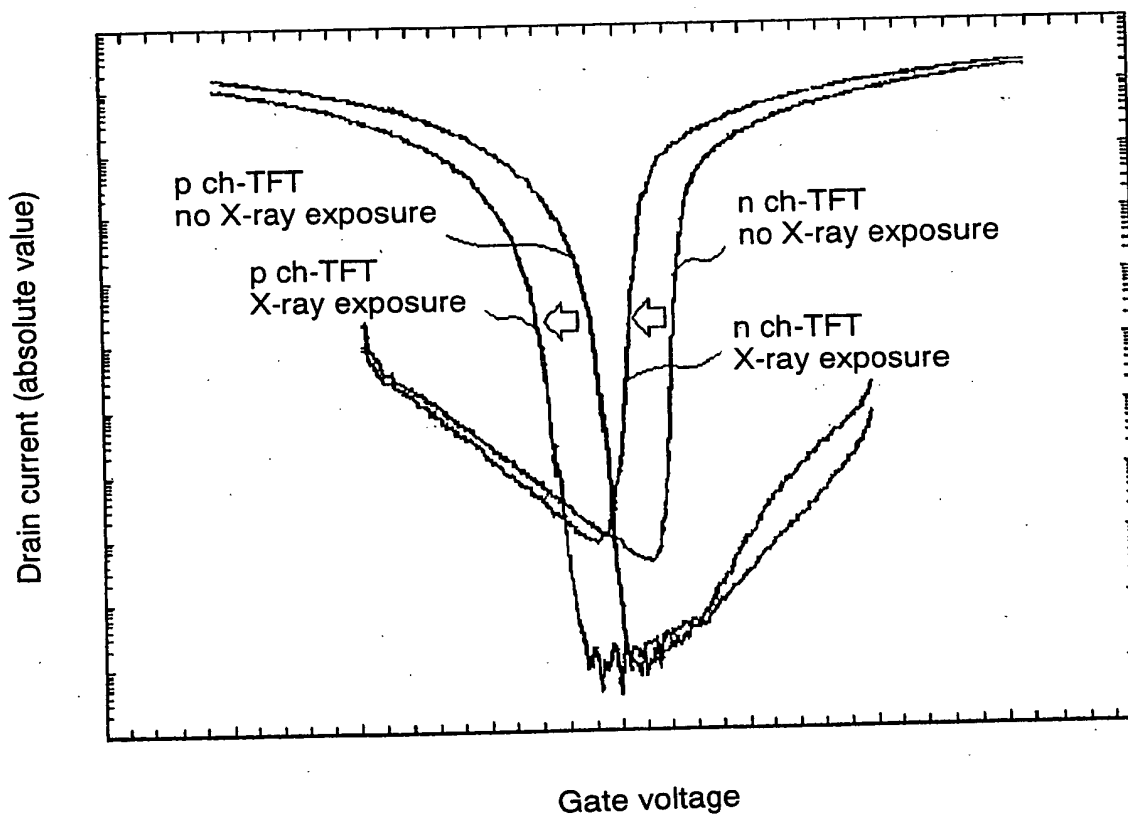


Fig. 7A

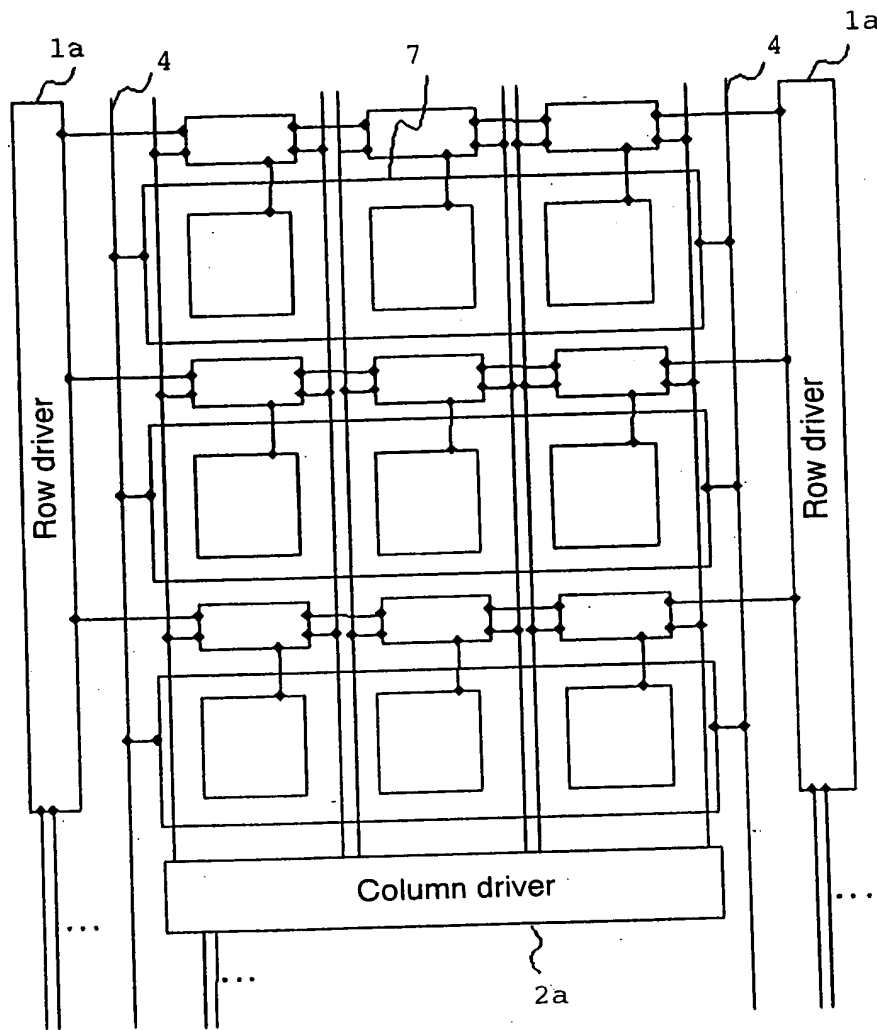
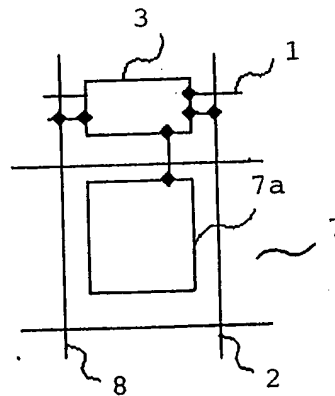


Fig. 7B



This cross-sectional view illustrates a semiconductor device with a central active region (5) enclosed by a gate structure (6, 7). The device is connected to row drivers at the top and bottom. Labels include 10a, 13a, 3, 4, 6, 7, A', 5, 11, 12, L1, L2, and A. Arrows indicate connections to row drivers.

Fig. 9A

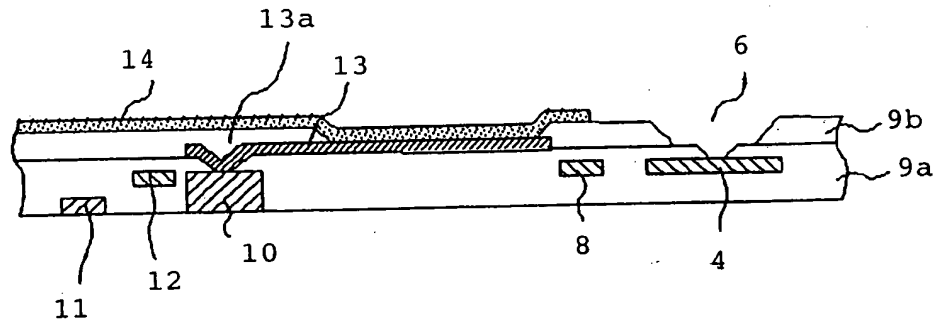
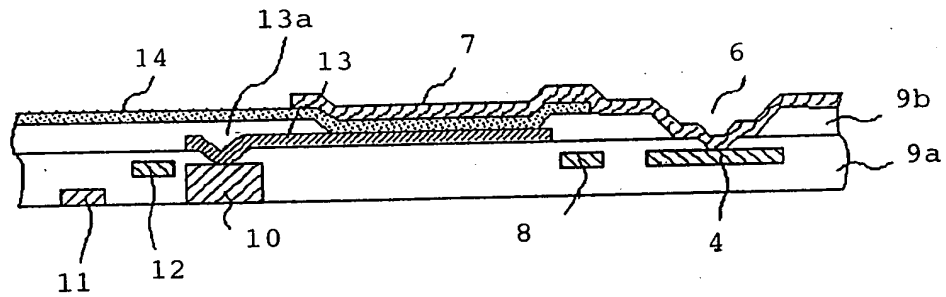


Fig. 9B





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Fig. 10

